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** CONTINUING DATA *****

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** FOREIGN APPLICATIONS *****

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35 USC 119 (a-d) conditions met Verified and Acknowledged Examiner's Signature <i>JTA</i>	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Initials <i>JTA</i>	JAPAN	DRAWING 19	51	5

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TITLE

Semiconductor memory device and semiconductor device and semiconductor memory device control method

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